

Fig. 1

Fig. 2A

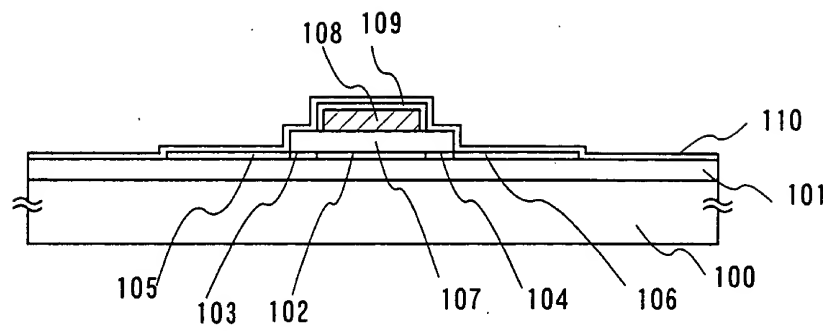


Fig. 2B

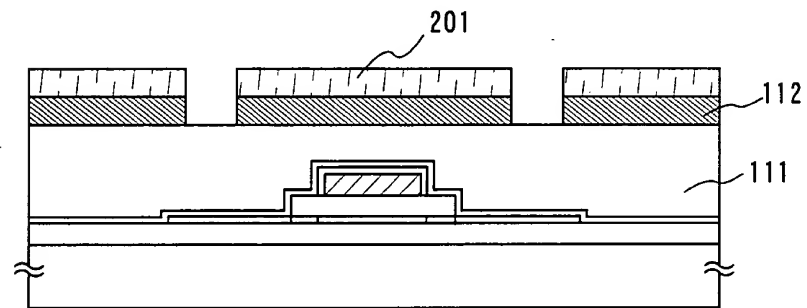


Fig. 2C

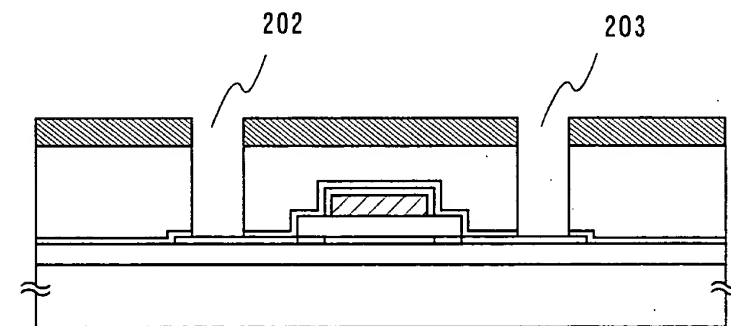


Fig. 3A

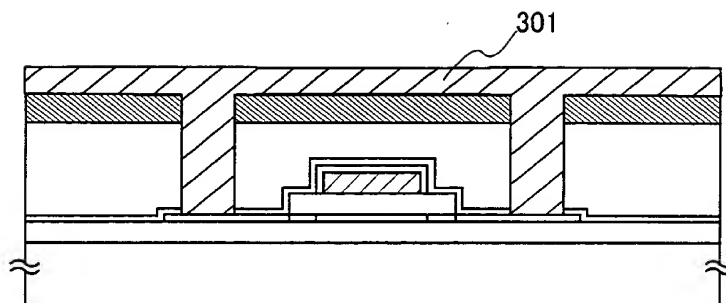


Fig. 3B

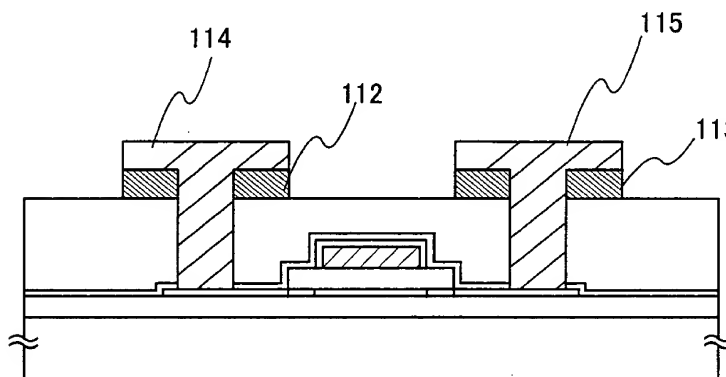
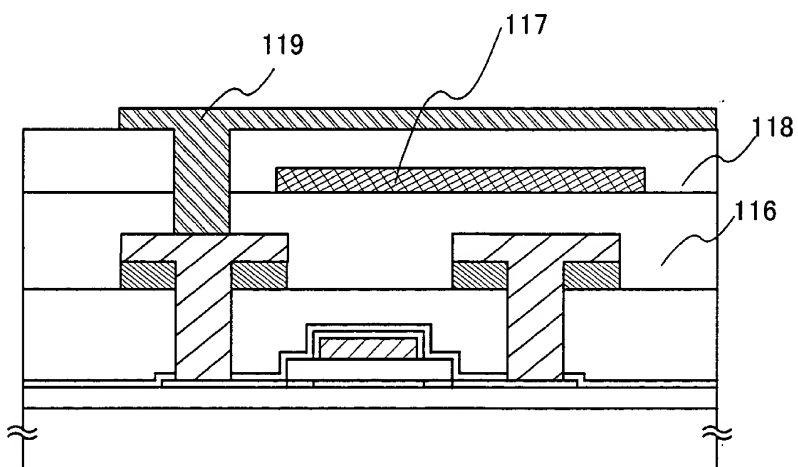


Fig. 3C



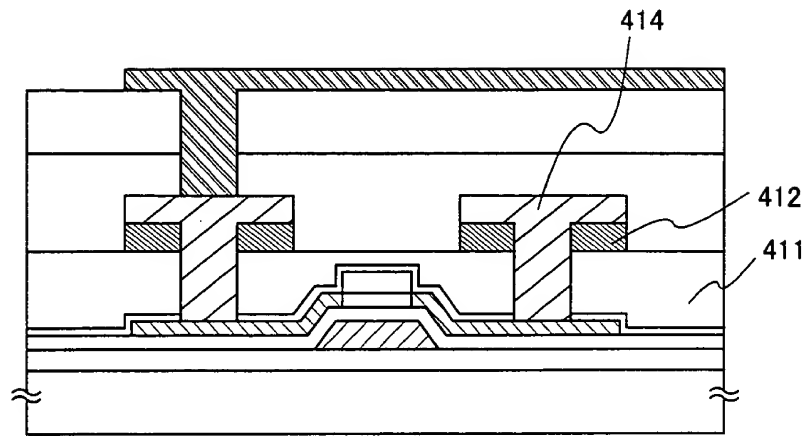


Fig. 4

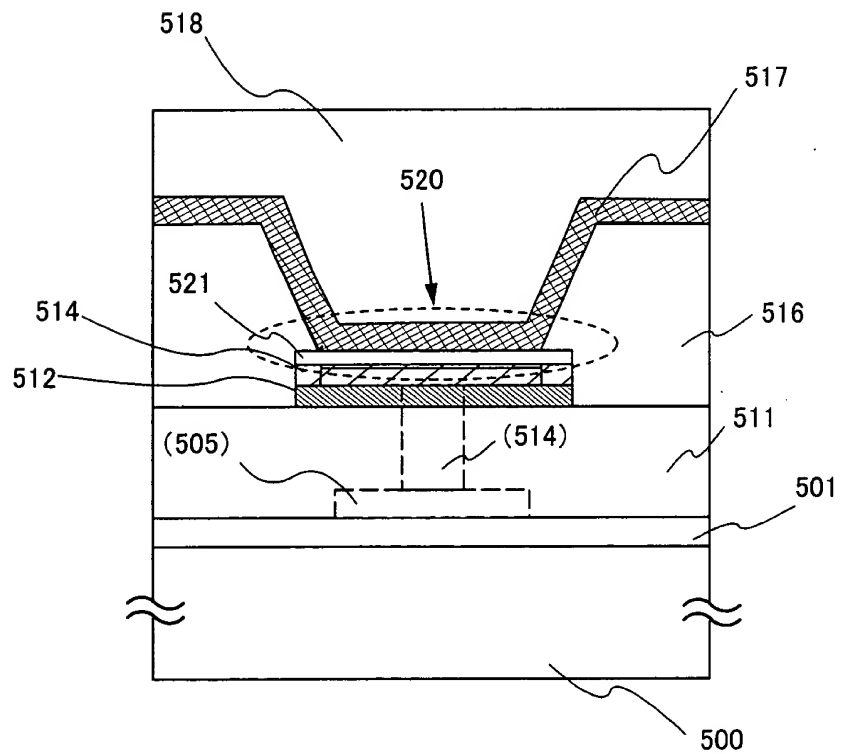


Fig. 5

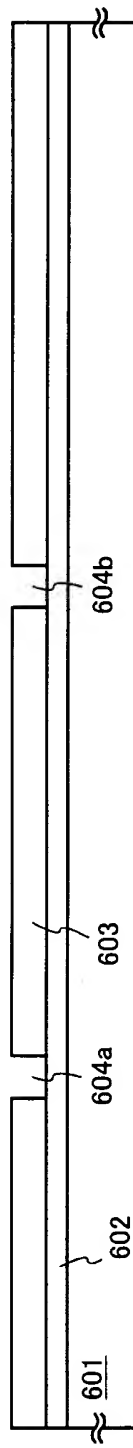


Fig. 6A

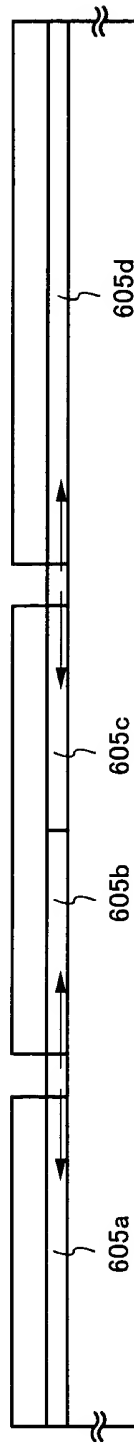


Fig. 6B

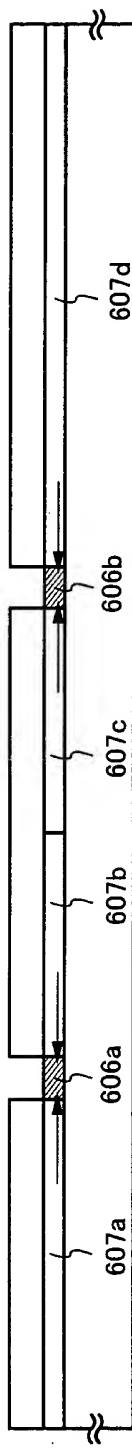


Fig. 6C

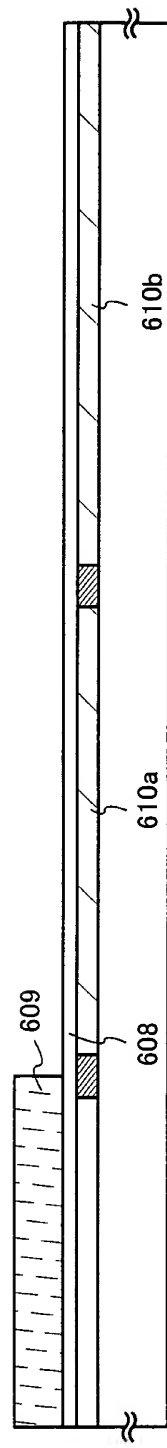


Fig. 6D

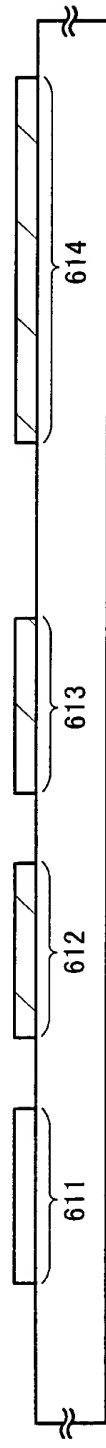


Fig. 6E

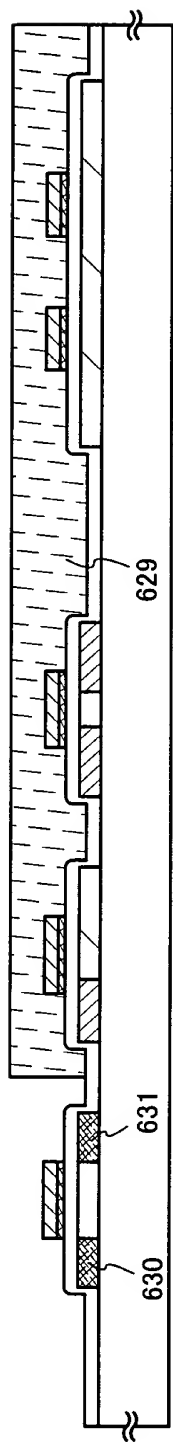
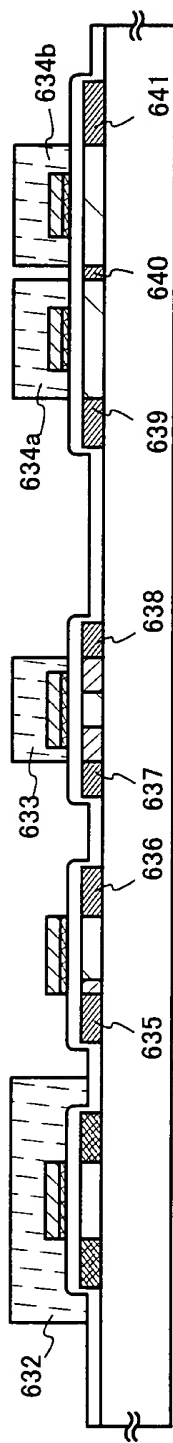
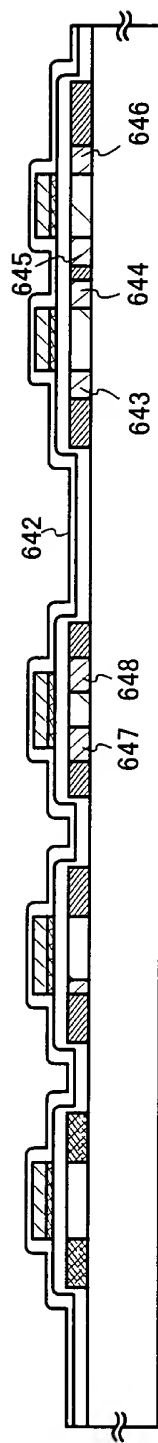


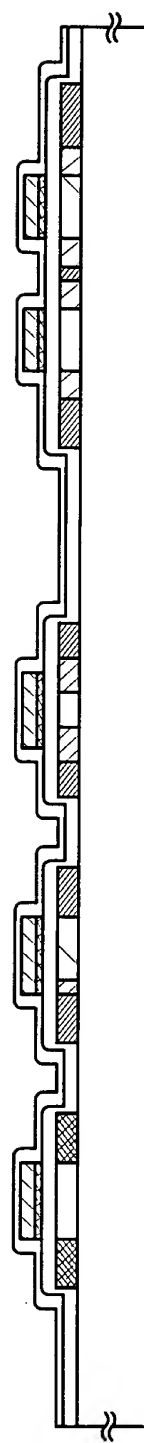
Fig. 8A



Fi. 8B



Fi 8C



Fi 8D

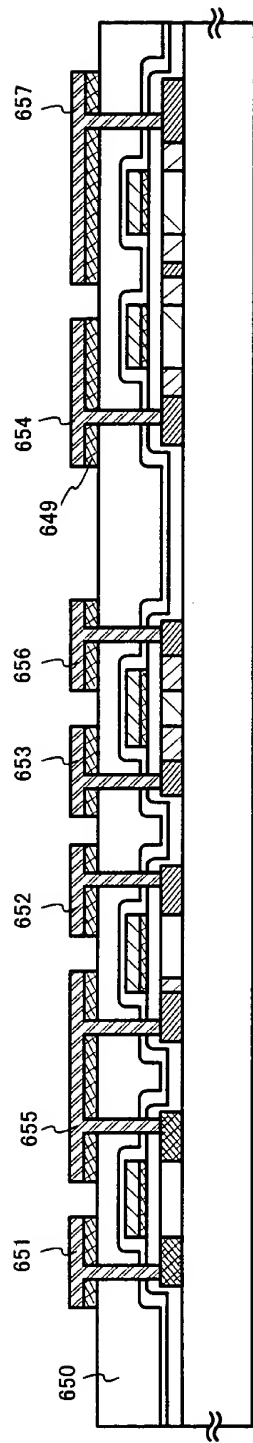
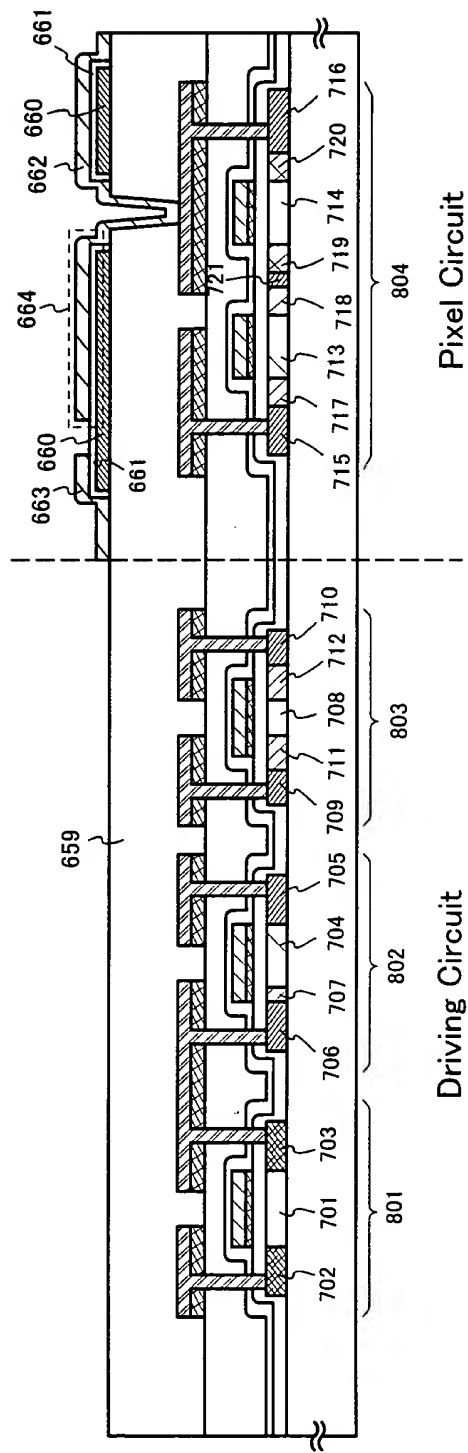


Fig. 9A



Fi. 9B

Pixel Circuit

Driving Circuit

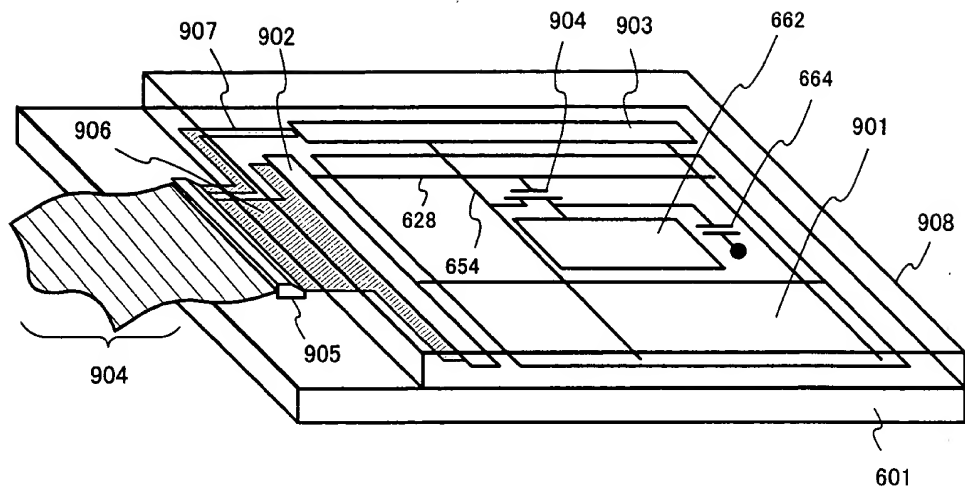


Fig. 10

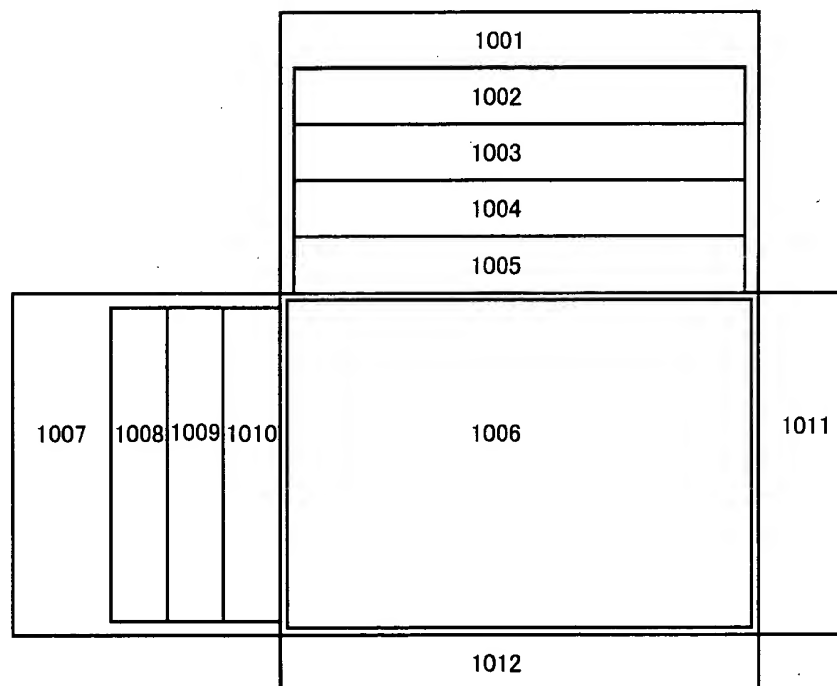


Fig. 11

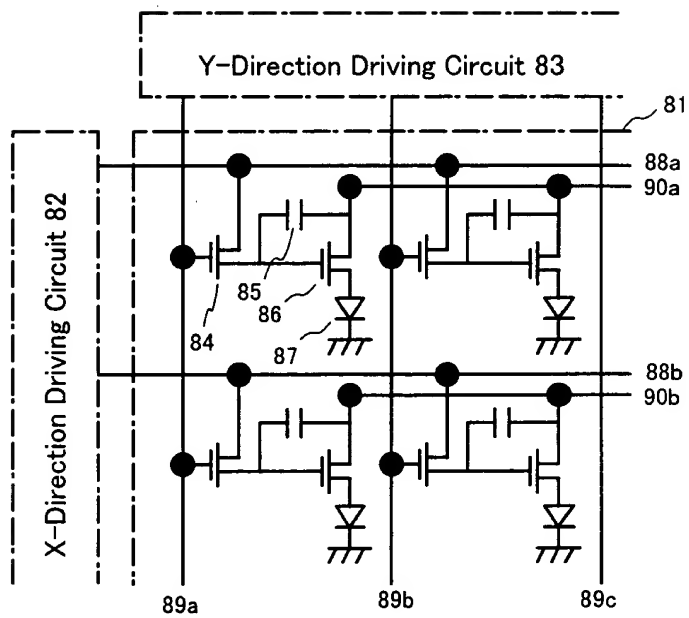


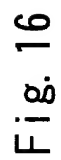
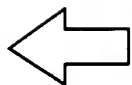
Fig. 12

This cross-sectional view shows a multi-layered assembly. At the bottom is a base layer 4001. Above it is a layer 4307 containing components 4005 and 4306. A central section 4201 contains components 4302 and 4202. To the right, another section 4202 contains components 4302 and 4202. The top layer 4102 is covered by a protective layer 4103. Various other components are labeled: 4104 at the top corners, 4101 at the top edges, 4303 in the center, 4305 and 4304 in the right section, 4301 in the right section, and 4006 on the left side. The assembly is shown in a cross-section from A to A.

Fig. 13B

Fig. 15B

LIGHT



Fi. 16

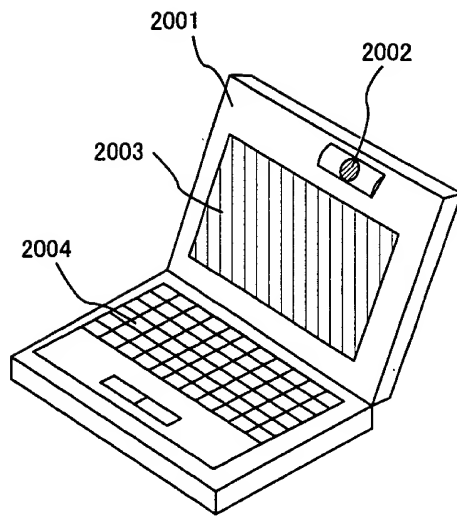


Fig. 18A

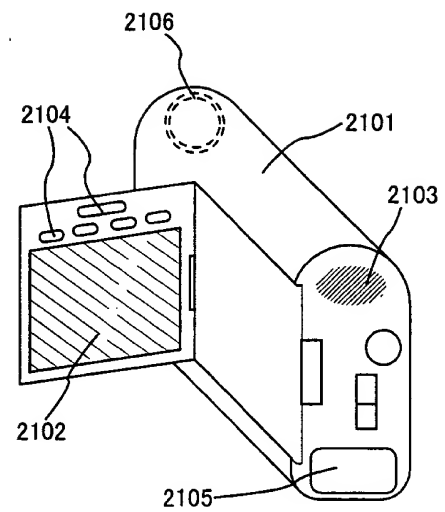


Fig. 18B

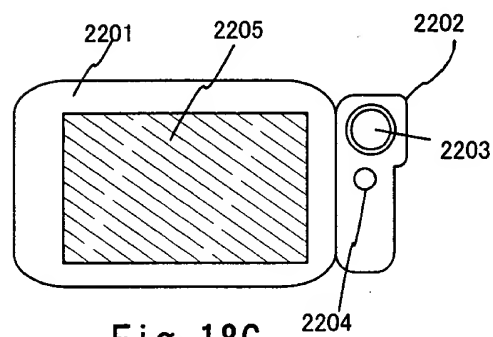


Fig. 18C

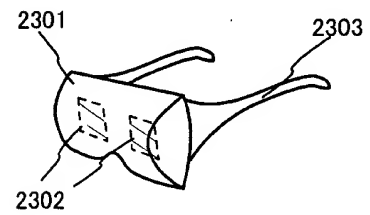


Fig. 18D

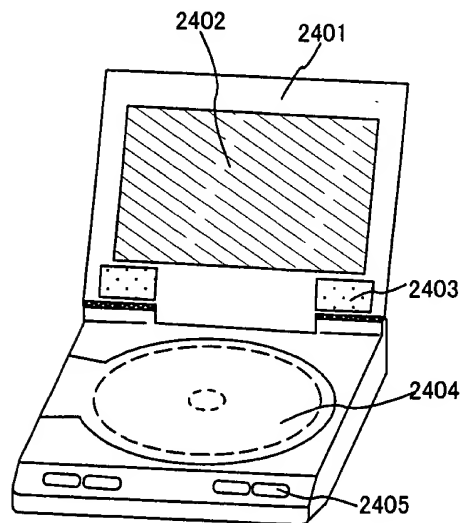


Fig. 18E

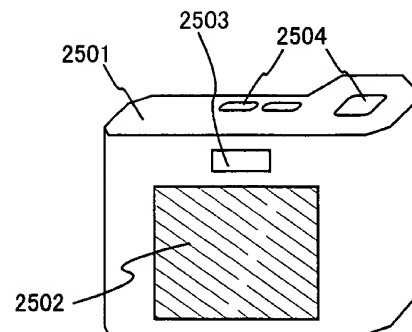


Fig. 18F

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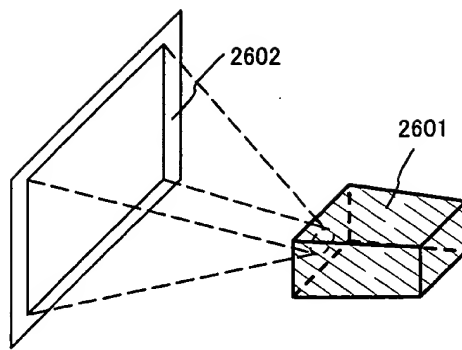


Fig. 19A

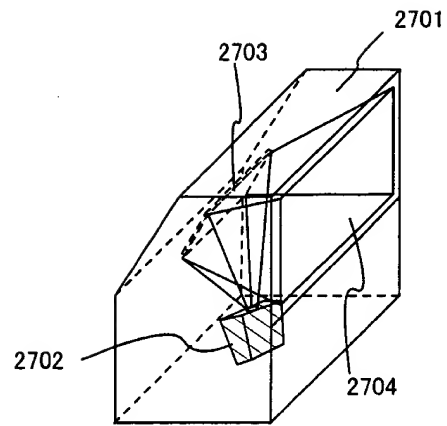


Fig. 19B

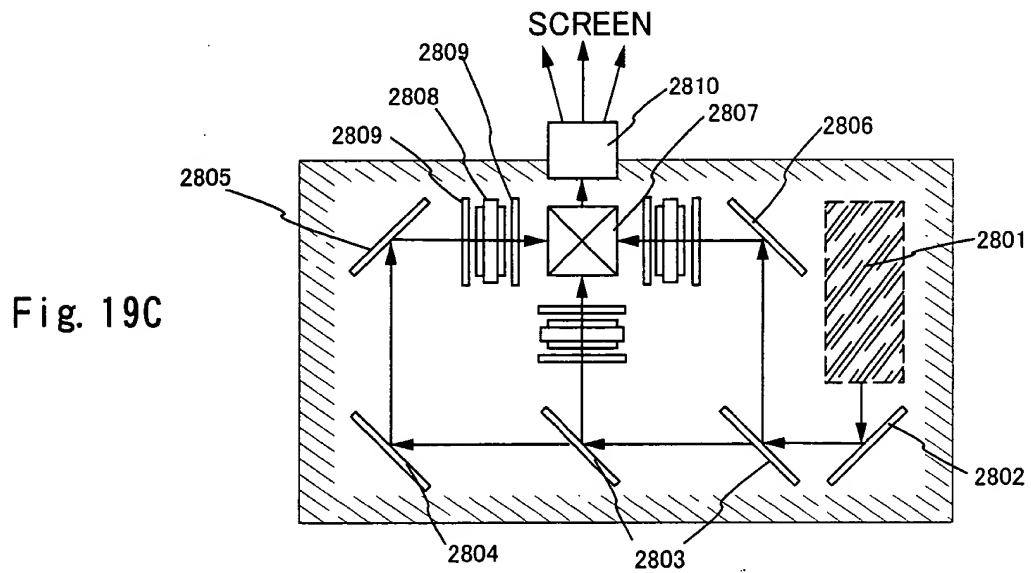


Fig. 19C

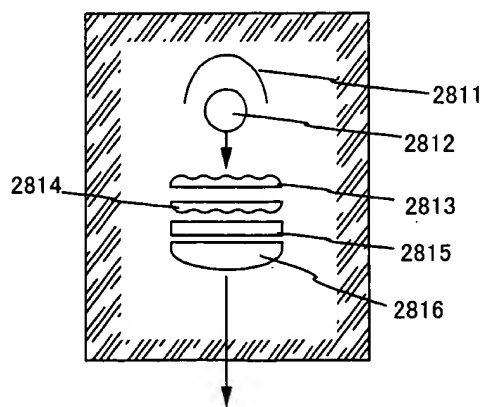


Fig. 19D

Fig. 20A

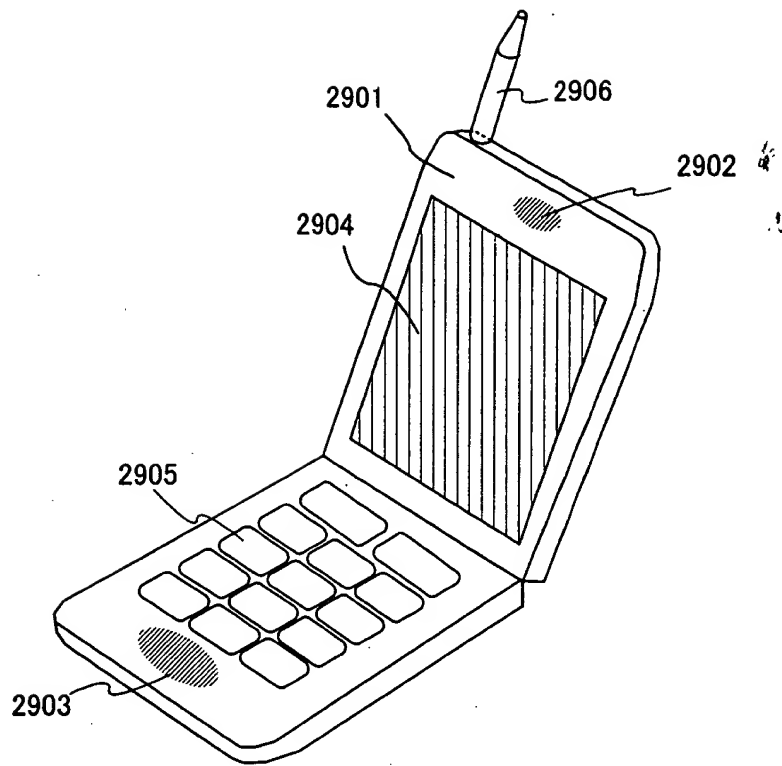


Fig. 20B

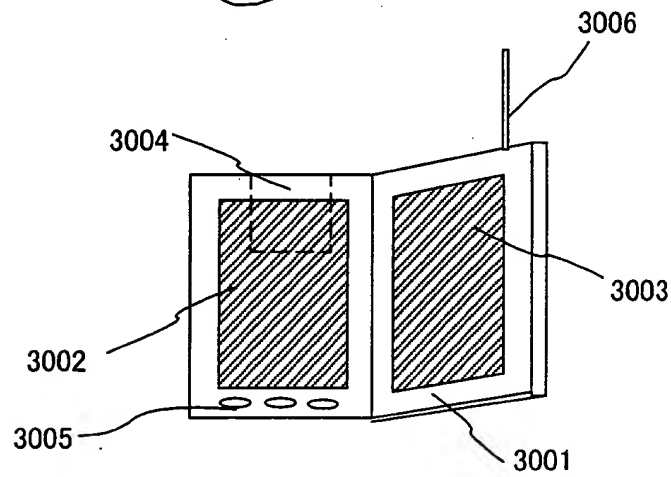


Fig. 20C

